

IN THE CLAIMS:

Claims 1-17 (Canceled).

Claim 18 (Currently Amended): A liquid crystal display device, comprising:

a substrate;

a first metal layer disposed on the substrate, the first metal layer includes a gate line connected to a gate electrode, and a first capacitor electrode;

an insulating layer covering the first metal layer;

a silicon layer disposed on the insulating layer, a portion of the silicon layer includes an active layer disposed over the gate electrode;

a second metal layer disposed on the silicon layer, the second metal layer includes a data line, a source electrode, a drain electrode, and a second capacitor electrode;

a passivation layer covering the second metal layer, a side edge portion of the drain electrode and the silicon layer being exposed from the passivation layer; and

a pixel electrode disposed on the passivation layer, the pixel electrode contacting the side edge portion of the drain electrode and the silicon layer,

wherein the pixel electrode includes a first end portion overlapping the silicon layer and laterally spaced apart from the gate electrode to not overlap the gate electrode.

Claim 19 (Original): The liquid crystal display device according to claim 18, wherein the source electrode electrically connects with the data line, the drain electrode is spaced apart from the source electrode, the source and drain electrodes are disposed on the active layer, and the second capacitor electrode is disposed over the first capacitor electrode

Claim 20 (Withdrawn): A halftone mask, comprising:

a light shielding portion shielding a photoresist from incident rays of light;

a semi-transmissive portion transmitting at least a portion of the incident rays of light to the photoresist; and

a light transmissive portion transmitting at least all the incident rays of light to the photoresist.

Claim 21 (Withdrawn): The halftone mask according to Claim 20, wherein the light shielding portion includes at least an opaque metal material having a low reflectivity.

Claim 22 (Withdrawn): The halftone mask according to Claim 20, wherein the opaque metal includes at least a chromium (Cr) material.

Claim 23 (Withdrawn): The halftone mask according to Claim 20, wherein the semi-transmissive portion includes at least a molybdenum silicide (MoSi) material.

Claim 24 (Withdrawn): The halftone mask according to Claim 23, wherein the molybdenum silicide (MoSi) material has a transmissivity of 30 to 40 %.

Claim 25 (Withdrawn): The halftone mask according to Claim 20, wherein the light shielding portion includes at least a molybdenum silicide (MoSi) material layer and a chromium (Cr) material layer sequentially disposed on a transparent substrate.

Claim 26 (Currently Amended): A liquid crystal display device, comprising:

- a substrate;

- a first metal layer disposed on the substrate, the first metal layer includes at least a gate line that is connected to a gate electrode, and a first capacitor electrode, one end of the gate line is electrically connected to a gate pad;

- an insulating layer covering the first metal layer;

- a gate pad contact hole formed passing through the insulating layer to uncover a portion of the gate pad;

- a silicon layer disposed on the insulating layer, a portion of the silicon layer includes an active layer disposed over the gate electrode;

- a second metal layer disposed on the silicon layer, the second metal layer includes at least a data line, a source electrode, a drain electrode, a second capacitor electrode, and a data pad;

a passivation layer covering the second metal layer, a side edge portion of the drain electrode and the silicon layer being exposed from the passivation layer; and

a pixel electrode disposed on the passivation layer, the pixel electrode contacting the side edge portion of the drain electrode and the silicon layer,

wherein the pixel electrode includes a first end portion overlapping the silicon layer and laterally spaced apart from the gate electrode to not overlap the gate electrode.

Claim 27 (Original): The liquid crystal display device according to Claim 26, wherein the source electrode electrically connects with the data line, the drain electrode is spaced apart from the source electrode, the source electrode and the drain electrode are disposed on the active layer, the second capacitor electrode is disposed over the first capacitor electrode, and the data pad is connected to first end of the data line.

Claim 28 (Original): The liquid crystal display device according to Claim 26, wherein a capacitor contact hole and a data pad contact hole are formed passing through the passivation layer uncovering corresponding portions of the second capacitor electrode and data pad.